Appln No.: 09/942,102 Applicant(s): William R. Wheeler et al. GENERATING A LOGIC DESIGN



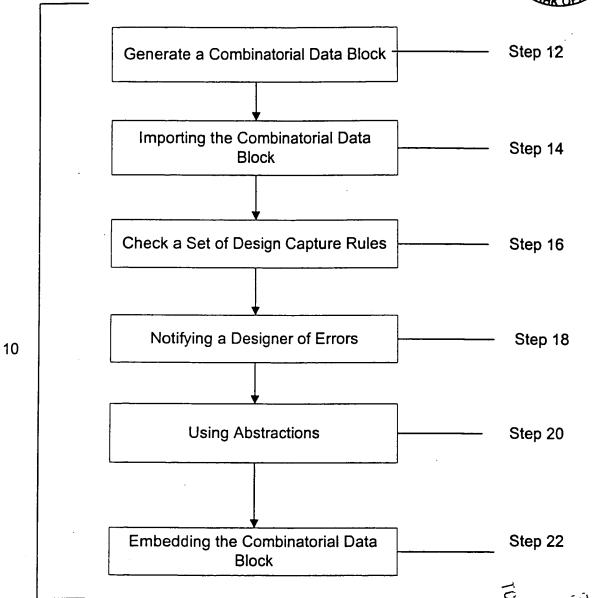


FIG. 1

